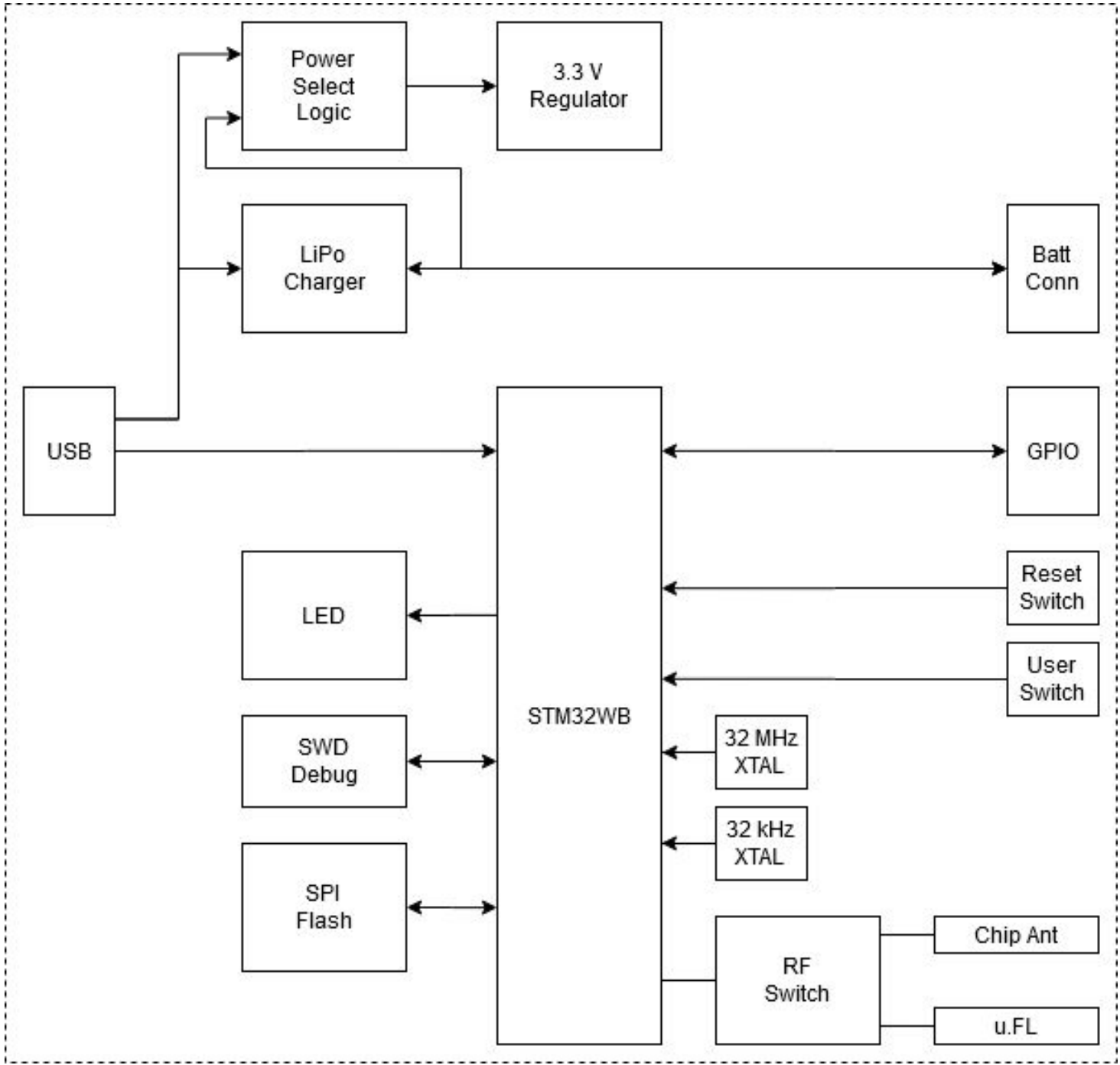
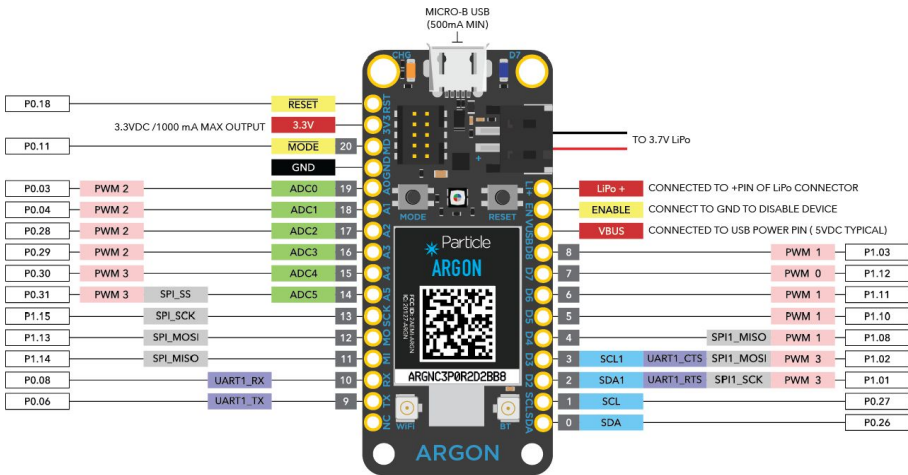
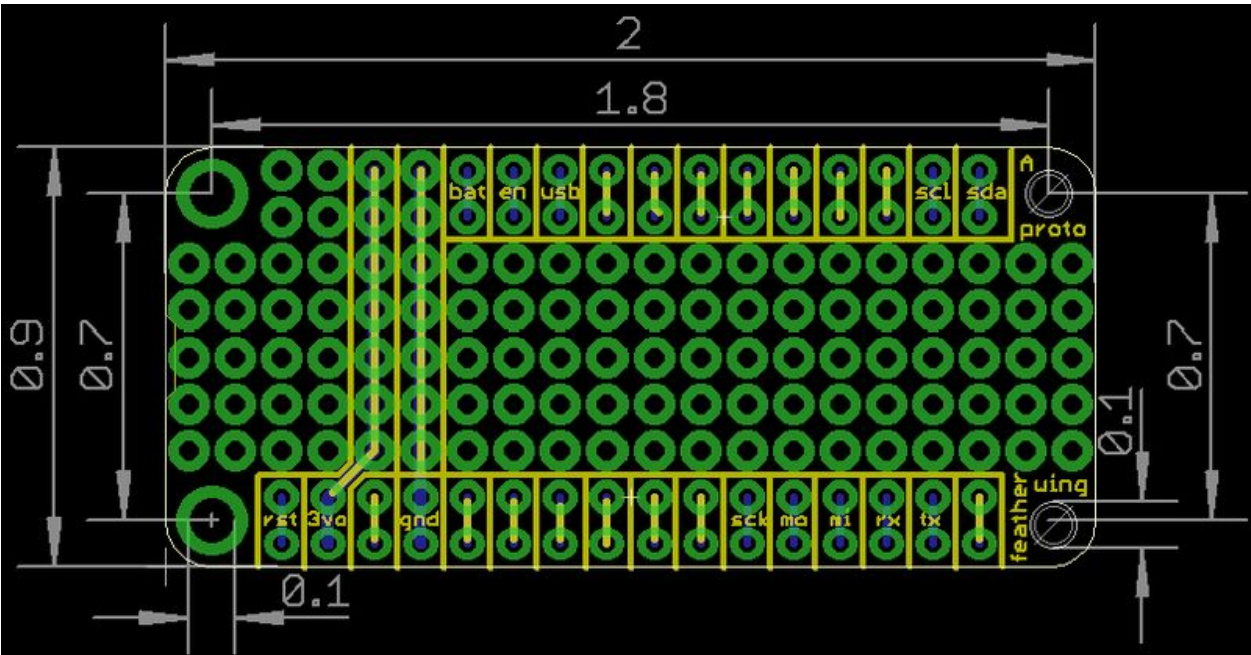
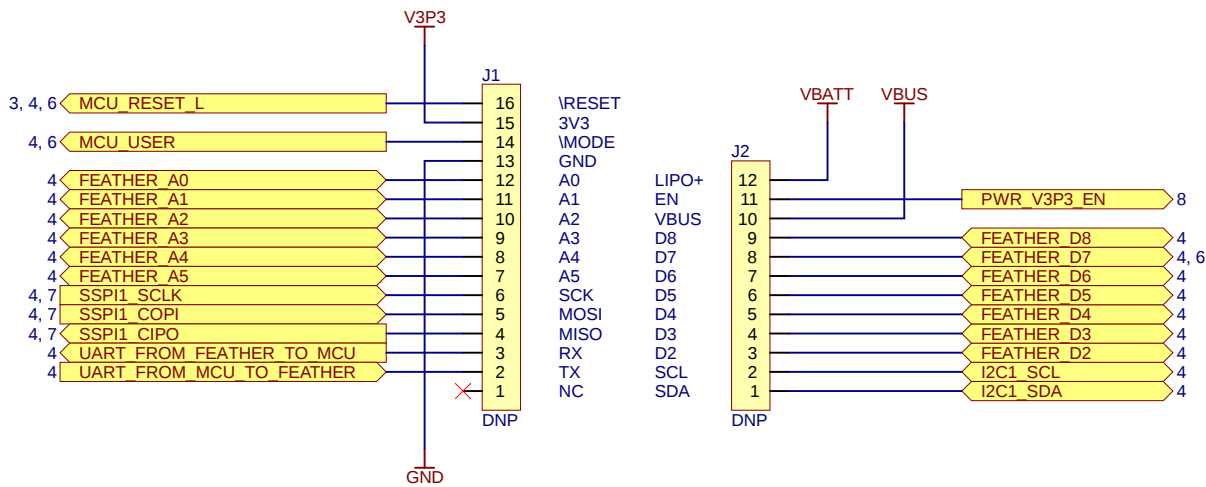






v1.0



Project: STM32WB Feather Board

Document: Main Header Connectors

Revision: 2

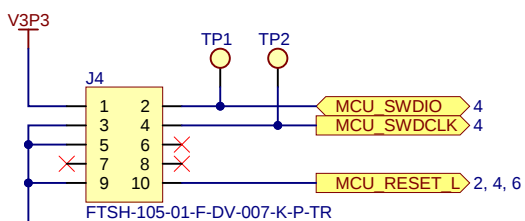
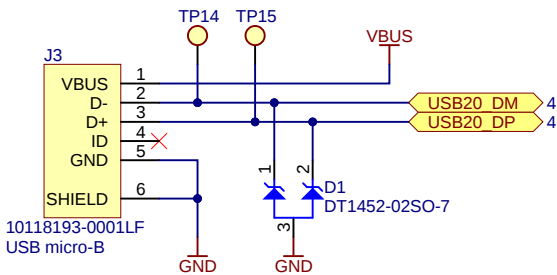
Drawn By: Jason Cerundolo

Date: 10/30/2020 Time: 00:10:56 Sheet 2 of 8

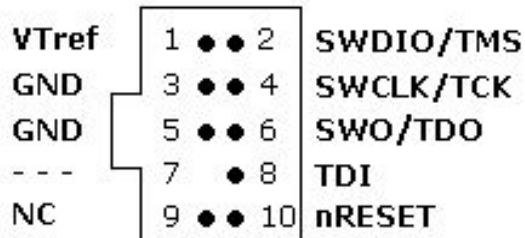
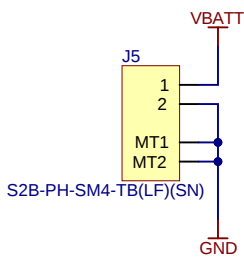
Proprietary and Confidential

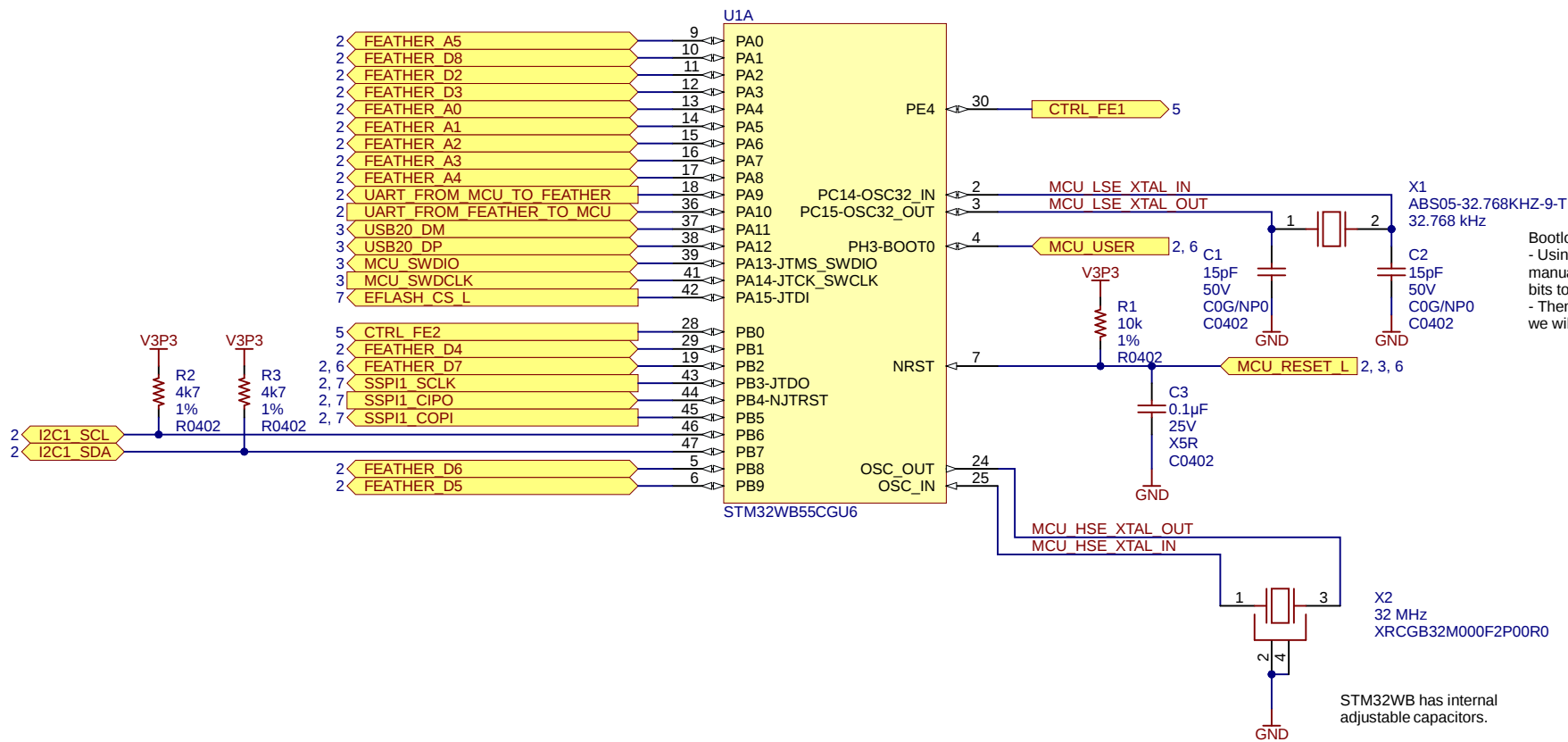


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The SWJ-DP incorporates pull-up resistors on JTDI, JTMS/SWDIO and nJTRST, as well as a pull-down resistor on JTCK/SWCLK.





Bootloader behavior:
- Using Table 1 from Section 2.2 of the reference manual. Set the nBOOT1 and nSWBOOT0 option bits to 1, and disable BOOT_LOCK.
- Then, if the User Button is pressed during reset, we will jump to the ROM bootloader.

STM32WB has internal adjustable capacitors.

Project: STM32WB Feather Board

Document: STM32WB GPIO

Revision: 2

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Date: 10/30/2020 Time: 00:10:57 Sheet 4 of 8

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A

B

C

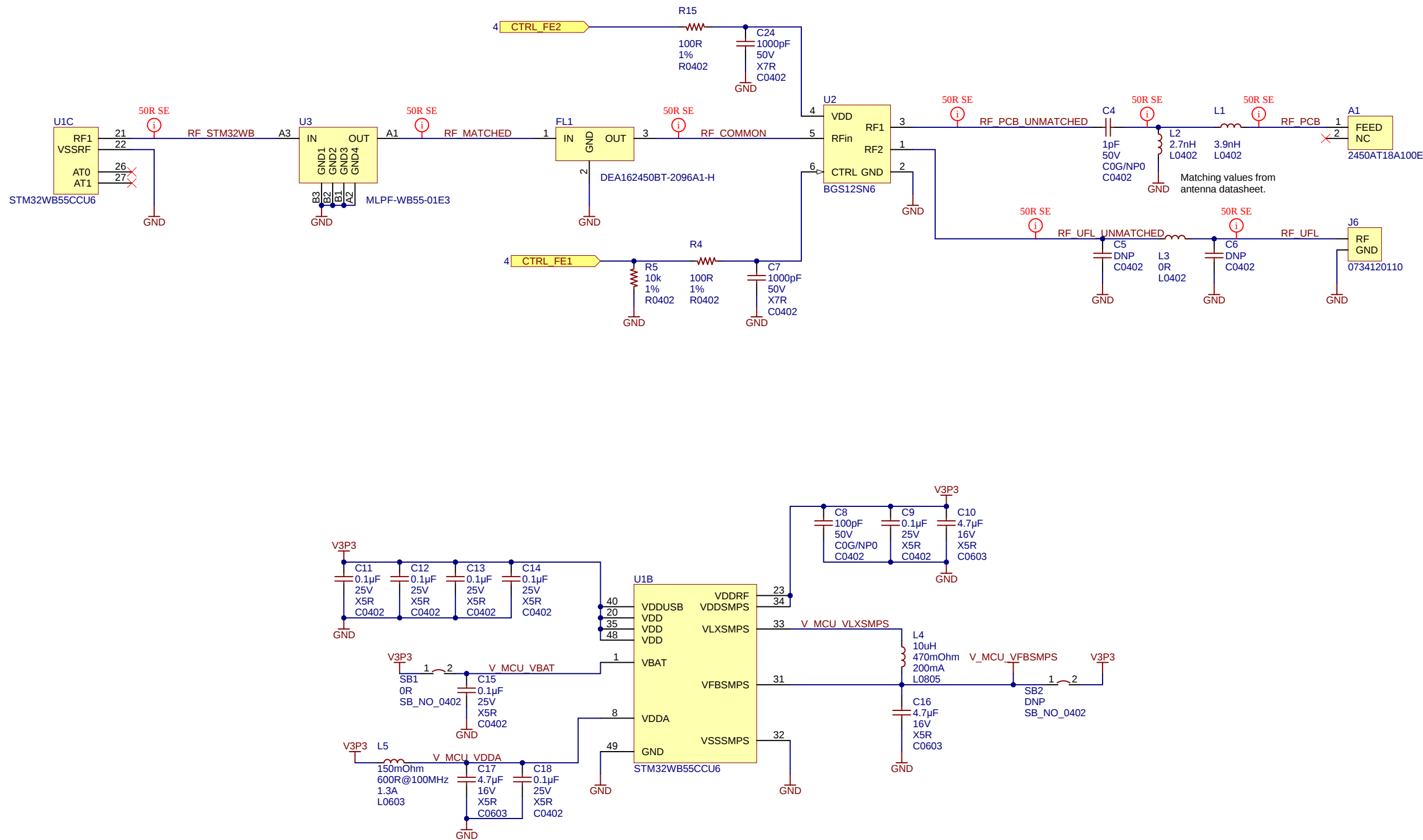
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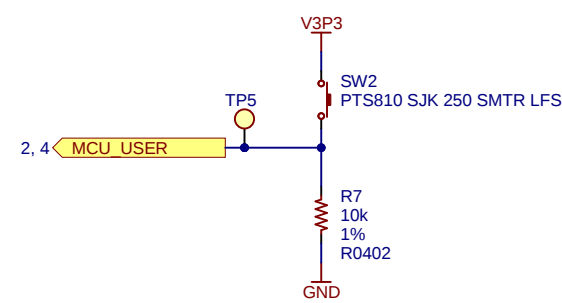
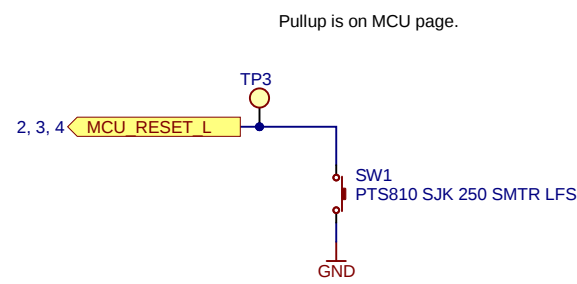
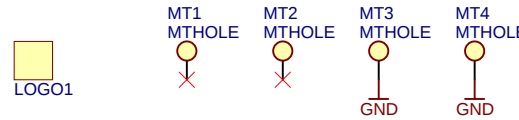
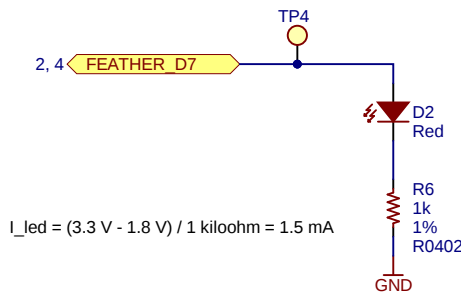
A

B

C

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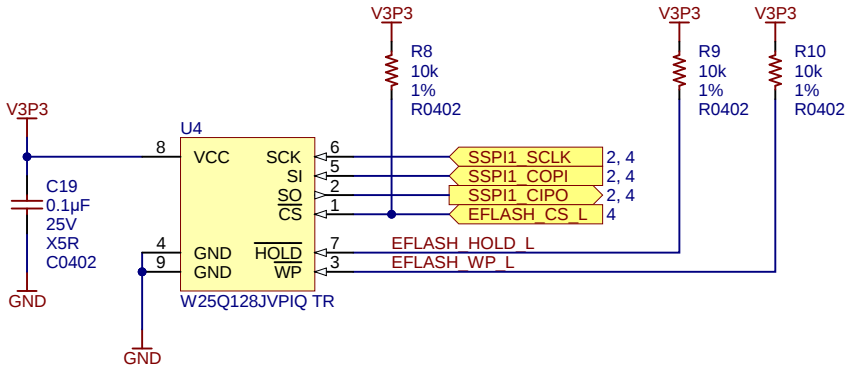




Assuming...
nBOOT1 = 1
nSWBOOT0 = 1
Main Flash not empty

USR Button Pressed = ROM Bootloader
USR Button Released = Main Flash

More info, see Section 2.3 of datasheet.



Project: **STM32WB Feather Board**

Document: **External Flash**

Revision: **2**

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Date: **10/30/2020** Time: **00:10:59** Sheet **7** of **8**

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